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1 **On crossbar switch and multiple bus interconnection networks with overlapping connectivity**
Wilkinson, B.;
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2 **Cost-performance analysis of cascaded crossbar interconnected multiprocessors**
Evequoz, C.;
Computers and Digital Techniques, IEE Proceedings- , Volume: 142 , Issue: 2 , March 1995
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On crossbar switch and multiple bus interconnectio networks with overlapping connectivity

Wilkinson, B.
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This paper appears in: **Computers, IEEE Transactions on**

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Abstract:
Multiprocessor interconnection networks are proposed which have the charact each **processor** or **memory** module can connect to a **group** of its near neigl adjacent directly connected **groups** include some of the same **processors** or **memories**. These overlapping connectivity networks are attractive especially large number of **processors** which cannot be provided with full connectivity t operate by communication between **processors**. Applications for overlapping connectivity networks include neural computers and dataflow computers. Ban formulas are derived using a probabilistic approach, including when intermedi **processors** are used in the interconnection path. A general cell design is pre capable of representing various overlapping connectivity networks

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